

	Type	L #	Hits	Search Text	DBs
5	BRS	L5	0	(first adj input adj signal with second adj input adj signal and second adj (DAC digital adj analog adj convert\$3)).clm.	US- PGPUB
7	BRS	L7	0	(second adj adjusting adj circuit with quadrature adj phase).clm.	US- PGPUB
8	BRS	L8	0	(second adj adjusting adj circuit with positive adj signal with negative adj signal).clm.	US- PGPUB
10	BRS	L10	0	(second adj control adj voltage with quadrature adj phase with second adj digital adj value).clm.	US- PGPUB
11	BRS	L11	0	(second adj control adj voltage with positive adj signal with negative adj signal).clm.	US- PGPUB
12	BRS	L12	0	(second adj input adj signal with quadrature adj phase and second adj digital adj value).clm.	US- PGPUB